# 10V Drive Nch MOS FET RDX045N60

### Structure

Silicon N-channel MOS FET

# ● Features

- 1) Low on-resistance.
- 2) Low input capacitance.
- 3) Excellent resistance to damage from static electricity.

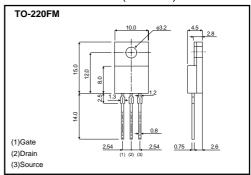
# Applications

Switching

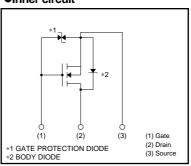
### Packaging specifications

	Package	Bulk
Type	Code	_
	Basic ordering unit (pieces)	500
RDX045N60		0

# ●External dimensions (Unit : mm)



### ●Inner circuit



# ●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol		Limits	Unit		
Drain-source voltage		Voss		600	V		
Gate-source voltage		Vgss		±30	V		
Drain augrant	Continuous	ID	*1	±4.5	А		
Drain current	Pulsed	I <sub>DP</sub>	*2	±18	Α		
Source current (Body diode)	Continuous	ls		4.5	Α		
	Pulsed	I <sub>SP</sub>	*2	18	Α		
Avalanche current		I <sub>AS</sub>	*3	4.5	Α		
Avalanche energy		Eas	*4	40	mJ		
Total power dissipation (Tc=25°C)		PD		35	W		
Channel temperature		Tch		150	°C		
Range of storage temperature		Tstg		-55 to +150	°C		

<sup>\*1</sup> Limited only by maximum temperature allowed \*3 L  $\rightleftharpoons$  3.4mH VDD=90V Rg=25 $\Omega$  \*4 L  $\rightleftharpoons$  3.4mH VDD=90V Rg=25 $\Omega$  starting Tch=25°C

# ●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to case	Rth(ch-c)	3.57	°C/W

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# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	±10	μΑ	V <sub>GS</sub> = ±25V, V <sub>DS</sub> =0V
Drain-source breakdown voltage	V <sub>(BR) DSS</sub>	600	_	_	V	I <sub>D</sub> = 1mA, V <sub>GS</sub> =0V
Zero gate voltage drain current	IDSS	_	_	25	μΑ	V <sub>DS</sub> = 600V, V <sub>GS</sub> =0V
Gate threshold voltage	V <sub>GS (th)</sub>	2.0	_	4.0	V	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA
Static drain-source on-state resistance	R <sub>DS (on)</sub> *	_	1.6	2.1	Ω	I <sub>D</sub> = 2.25A, V <sub>GS</sub> = 10V
Forward transfer admittance	Y <sub>fs</sub>   *	1.5	2.8	-	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 2.25A
Input capacitance	Ciss	_	500	_	pF	Vps= 25V
Output capacitance	Coss	_	60	_	pF	Vgs=0V
Reverse transfer capacitance	Crss	-	10	_	pF	f=1MHz
Turn-on delay time	t <sub>d (on)</sub> *	-	18	_	ns	V <sub>DD</sub> ≒ 150V
Rise time	tr *	-	16	_	ns	I <sub>D</sub> = 2.25A V <sub>G</sub> s= 10V
Turn-off delay time	t <sub>d (off)</sub> *	-	36	_	ns	R <sub>L</sub> = 66.7Ω
Fall time	t <sub>f</sub> *	-	28	_	ns	R <sub>G</sub> =10Ω
Total gate charge	Qg *	_	16	_	nC	V <sub>DD</sub> ≒300V, V <sub>GS</sub> =10V
Gate-source charge	Qgs *		4	_	nC	I <sub>D</sub> = 4.5A
Gate-drain charge	Q <sub>gd</sub> *	_	6	_	nC	$R_L=66.7\Omega$ , $R_{GS}=10\Omega$

\*Pulsed

# ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp *	_	_	1.5	V	I <sub>S</sub> = 4.5A, V <sub>GS</sub> =0V
Reverse recovery time	trr	-	400	_	ns	IDR= 4.5A, VGS=0V
Reverse recovery charge	Qrr	_	4.4	_	μC	di/dt= 100A / μs

<sup>\*</sup>Pulsed

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